

Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet

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of

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Complete if Known

Application Number	10/020,388
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Filing Date	December
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First Name of Inventor	William Salkewicz
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Art Unit	2152
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Examiner Name	Not yet assigned
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Attorney Docket Number

4906.Pool Technology Center 2100

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Date
Considered

2/07/02

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Sheet 1 of 4

Application Number	10/020,388
Filing Date	December 14, 2001
First Named Inventor	William Salkiewicz
Art Unit	2152
Examiner Name	Not yet assigned
Attorney Docket Number	4906.P001D

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9/01/04

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/020,388
		Filing Date	December 14, 2001
		First Named Inventor	William Salkewicz
		Group Art Unit	2152
		Examiner Name	Not yet assigned
		Attorney Docket Number	4906.P001D
Sheet	2	of	4

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Examiner Initials ²	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
h		Adve, V., et al., "Performance Analysis of Mesh Interconnection Networks with Deterministic Routing," IEEE Transactions on Parallel and Distributed Systems, Vol. 5, No. 3, pp. 225-246, March 1994.	
h		Almquist, P., "Towards Requirements for IP Routers," Network Working Group, RFC 1716, November 1994.	
h		Austin, T., et al., "Efficient Detection of All Pointer and Array Access Errors," Proceedings of the ACM Conference on Programming Language Design and Implementation, pp. 290-301, June 1994.	
h		Bolla, R., et al., "A neutral Strategy for Optimal Multiplexing of Circuit-and-Packet-Switched Traffic," GLOBECOM '92 IEEE Communication Society, pp. 1324-1330, December 1992.	
h		Brooks, R., et al., "An Optimizing Compiler for Lexically Scoped LISP," ACM Symposium on LISP and Functional Programming, pp. 261-275, August 1982.	
h		Buzen, J.P., et al., "The Evolution of Virtual Machine Architecture," AFIPS National Computer Conference, pp. 291-299, June 1973.	
h		Comer, D., "Computer Networks and Internets," Prentiss Hall, 1997.	
h		Cmelik, B., et al., "Shade: A Fast Instruction - Set Simulator for Execution Profiling," ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems, pp. 128-137, May 1994.	
h		Dally, W.J., et al., "Deadlock-Free Message Routing in Multiprocessor Interconnection Networks," IEEE Transactions on Computers, Vol. C-36, No. 5, pp. 547-553, May 1987.	
h		Dally, W.J., "Virtual-Channel Flow Control," Proc. 17th International Symposium on Computer Architecture, pp. 60-68, May 1990.	
h		Dally, W.J., "Performance Analysis of k-ary n-cube Interconnection Networks," IEEE Transactions on Computers, Vol. 39, No. 6, pp. 775-785, June 1990.	

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Sheet 3 of 4

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Application Number	10/020,388
Filing Date	December 14, 2001
First Named Inventor	William Salkewicz
Group Art Unit	2152
Examiner Name	Not yet assigned
Attorney Docket Number	4906.P001D

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u		Dally, W.J., et al., "Deadlock-Free Adaptive Routing in Multicomputer Networks Using Virtual Channels," IEEE Transactions on Parallel and Distributed Systems, Vol. 4, No. 4, pp. 466-475, April 1993.	
u		Davidson, J., et al., "Cint: A RISC Interpreter for the C Programming Language," Proceedings of the SIGPLAN '87 Symposium on Interpreters and Interpretive Techniques, pp. 189-197, June 1987.	
u		Deutsch, L.P., et al., "Efficient Implementation of the Smalltalk-80 System," Proceedings of the 11th Annual ACM Symposium on Principles of Programming Languages, pp. 297-302, January 1984.	
u		Duato, J., "Anew Theory of Deadlock-Free Adaptive Routing in Wormhole Networks," IEEE Transactions on Parallel and Distributed Systems, Vol. 4, No. 12, pp. 1320-1331, December 1993.	
u		Ellis, J., et al., "Safe, Efficient Garbage Collection for C++," Proceedings of the 1994 USENIX C++ Conference, pp. 142-177, April 1994.	
u		Fischer, C., et al., "The Implementation of Run-Time Diagnostics in Pascal," IEEE Transactions on Software Engineering, Vol. SE-6, No. 4, pp. 313-319, July 1980.	
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u		Jesshope, C.R., et al., "High Performance Communications in Processor Networks," Proc. 16th International Symposium on Computer Architecture, pp. 150-157, May 1989.	
u		Kirkpatrick, S., et al., "Optimization by Simulated Annealing," Science, Vol. 220, No. 4598, pp. 671-680, May 1983.	

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Sheet 4 of 4

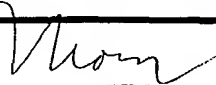
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u		Leinwand, A., et al., Cisco Router Configuration, MacMillan Technical Publishing, Indianapolis, IN, 1998.	
u		Li, K., et al., "Memory Coherence in Shared Virtual Memory Systems," ACM Transactions on Computer Systems, Vol. 7, No. 4, pp. 321-359, November 1989.	
u		Linder, D.H., et al., "An Adaptive and Fault Tolerant Wormhole Routing Strategy for k-ary n-cubes," IEEE Transactions on Computers, Vol. 40, No.1, pp. 2-12, January 1991.	
u		Luckham, D., et al., "Verification of Array, Record, and Pointer Operations in Pascal," ACM Transactions on Programming Languages and Systems, Vol. 2, No. 2, pp. 226-244, October 1979.	
u		May, C., MIMIC: A Fast System/370 Simulator," Proceedings of the SIGPLAN '87 Symposium in Interpreters and Interpretive Techniques, pp. 1-13, June 1987.	
u		Sites, R., et al., "Binary Translation," Communications of the ACM, Vol. 36, No. 2, pp.69-81, February 1993.	
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u		Yang, C.S., et al., "Performance Evaluation of Multicast Wormhole Routing in 2D-Torus Multicomputers," ICCI '92 IEEE Computer Society, pp. 173-178, May 1992.	
u		Yantchev, J., "Adaptive, Low Latency, Deadlock-Free Packet Routing for Networks of Processors," IEEE Proceedings, Vol. 136, Pt. E, No. 3, pp. 178-186, May 1989.	

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